Y. Ben Asher Publications

Journals: Names of authors are in alphabetical order.


0.0.1 Accepted for Publication


50. Y. Ben-Asher and Nadav Rotem, *type legalization* Accepted to ACM TACO-2012-173.

Conference papers:

1. Y. Ben-Asher, D. Egosi and A. Schuster, “SIMD Algorithms for 2-D Arrays in the Perfect Shuffle Network”, proc. 16th symposium on Computer Architecture, Jerusalem, May 1989. (A final version of this work appears also in section ?? item 4)


5. Y. Ben-Asher, D. Peleg, R. Ramaswami and A. Schuster, “The Power of Reconfiguration”, proc. 18th International Colloquium on Automata, Languages, and Programming (ICALP), pp. 139-150, Madrid, Spain, July 1991. (A final version of this work appears also in section ?? item 2)


10. Y. Ben-Asher and A. Schuster, “Efficient Address Decoding for Fast Packet Switching Systems”, Proc. 7th International Parallel Processing Symposium, April 1993, Newport Beach, L.A. (A final version of this work appears also in section ?? item 13)


12. Y. Ben-Asher, D. Gordon and A. Schuster, “Optimal Simulations in Reconfigurable Arrays”, 1st European Symposium on Algorithms, Bad Honnef Germany, September 1993. (A final version of this work appears also in section ?? item 8)


14. Y. Ben-Asher, A. Cohen and A. Schuster, “General Tradeoffs Between Size and Time in Reconfigurable Meshes”, 2nd Reconfigurable Architectures Workshop RAW’95, Santa Barbara California, 1995. (A final version of this work appears also in section ?? item 15)

15. Y. Ben-Asher and I. Newman, “Decision Trees with Threshold Queries” Structure in Complexity Theory Structure’95, Minneapolis USA, 1995. (A final version of this work appears also in section ?? item 12)


17. G. Haber and Y. Ben-Asher, “On the Usage of simulators to detect inefficiency of parallel programs caused by ”bad” schedulings: the SIMPARC approach”, HiPC (High performance computing), New Delhi, India, pp. 255-272, 1995. (A final version of this work appears also in section ?? item 16)

18. Y. Ben-Asher, E. Farchi and I. Newman, Optimal Search in Trees, 8th Annual ACM-SIAM Symposium on Discrete Algorithms 1997, (SODA97), New Orleans, Louisiana. (A final version of this work appears also in section ?? item 29)
19. Y. Ben-Asher and G. Haber, “Parallel Solutions of Indexed Recurrence Equations” IPPS’97, Geneva, pp. 413-427, 1997. (A final version of this work appears also in section ?? item 22)


21. Yosi Ben-Asher and Esti Stein, Basic Results in Automatic Transformations of Shared Memory Parallel Programs into Sequential Programs, Third Asian Computing Science Conference, Kathmandu, Nepal, 1997. (A final version of this work appears also in section ?? item 21)

22. A. Agbaria, Y. Ben-Asher, and I. Newman, The Partitioned PRAM Model, Re-Doing Basic PRAM Algorithms with Limited Resources, Symposium on Parallel Algorithms and Architectures 1999, (SPAA99), San-Malo, France. (A final version of this work appears also in section ?? item 27)

23. Y. Ben-Asher, and G. Haber, Efficient Parallel Solutions of Linear Algebraic Circuits, Symposium on Parallel Algorithms and Architectures 1999, (SPAA99), San-Malo, France. (A final version of this work appears also in section ?? item 22)

24. Y. Ben-Asher and E. Stein, “Basic Algorithms for the Asynchronous Reconfigurable Mesh”, 9th Reconfigurable Architectures Workshop (RAW 2002), Fort Lauderdale, Florida, 2002. (A final version of this work appears also in section ?? item 26)


26. Y. Ben-Asher, G. Haber and D. Citron, Overlapping Memory Operations with Circuit Evaluation in Reconfigurable Computing, The 11th Reconfigurable Architectures Workshop, Santa Fe, New Mexico, 2004. (A final version of this work appears also in section ?? item 32)

27. Y. ben-Asher, S. Berkovsky, E. Shmueli and A. Tammam, Using a J2EE Cluster for Parallel Computation of Join Queries in a Distributed Databases, The 3rd International Symposium on Parallel and Distributed Computing, Cork Ireland 2004. (A final version of this work appears also in section ?? item 30)

28. Y. ben-Asher, and S. Berkovsky, UNSO: Un Specified Ontologies for Peer-to-Peer Ecommerce Applications, International conference on Informatics (ICT’04), Cesme, Turkey 2004. (A final version of this work appears also in section ?? item 31)


35. Y. Ben-Asher and D. Meisler, *Towards a Source Level Compiler: Source Level Modulo Scheduling*, 5th Workshop on Compile and Runtime Techniques for Parallel Computing (CRTC) Ohio, 2006. (A final version of this work appears also in section ?? item 33)


37. Y. Ben Asher and M. Yuda, *Source Level Merging of Independent Programs*, PACT ’07: Proceedings of the 16th International Conference on Parallel Architecture and Compilation Techniques (poster), Brasov, Romania, 2007. (A final version of this work appears also in section ?? item 37)


44. Y. Ben-Asher and N. Rotem, *Binary Synthesis with Multiple Memory Banks Targeting Array References* FPL 2009, Prague, Czech Republic, August 2009.


49. Yosi Ben Asher and Eldar Fisher and Gady Haber and Vladislav Tar-takovsky *Fast Evaluation of Boolean Circuits Based on Two-Players Game and Optical Connectivity Circuit* The 41st International Conference on Parallel Processing (ICPP),Pittsburgh, 2012

50. Y. Ben-Asher and Gil Kulish, *Combining Cache Aware Scheduling with Lazy Threads* Conference on Parallel and Distributed Processing Techniques (PDPTA), USA, 2012.